

S/N Unknown

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Donald S. Gardner	Examiner:	Unknown
Serial No.:	Unknown	Group Art Unit:	Unknown
Filed:	Herewith	Docket:	884.140US3
Title:	INDUCTORS FOR INTEGRATED CIRCUITS, INTEGRATED CIRCUIT COMPONENTS, AND INTEGRATED CIRCUIT PACKAGES		
Assignee:	Intel Corporation	Customer No:	21186

INFORMATION DISCLOSURE STATEMENT

MS Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicant respectfully requests that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicant requests that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicant with the next official communication.

Pursuant to 37 C.F.R. §1.97(b), it is believed that no fee or statement is required with the Information Disclosure Statement. However, if an Office Action on the merits has been mailed, the Commissioner is hereby authorized to charge the required fees to Deposit Account No. 19-0743 in order to have this Information Disclosure Statement considered.

Pursuant to 37 C.F.R. §1.98(d), copies of the listed documents are not provided as these references were previously cited by or submitted to the U.S. Patent Office in connection with Applicant's prior U.S. applications, Serial No. 10/238,539, filed on September 10, 2002, and/or Serial No. 09/444,608, filed on September 23, 1999, now issued as U.S. Patent No. 6,452,247, which are relied upon for an earlier filing date under 35 U.S.C. §120.

INFORMATION DISCLOSURE STATEMENT

Serial No :Unknown

Filing Date: Herewith

Title: INDUCTORS FOR INTEGRATED CIRCUITS, INTEGRATED CIRCUIT COMPONENTS, AND INTEGRATED CIRCUIT PACKAGES

Assignee: Intel Corporation

Page 2

Dkt: 884.140US3 (INTEL)

The Examiner is invited to contact the Applicant's Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

DONALD S. GARDNER

By his Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
Attorneys for Intel Corporation
P.O. Box 2938
Minneapolis, Minnesota 55402
(612) 349-9592

Date Feb. 6, 2004 By Ann M. McCrackin
Ann M. McCrackin
Reg. No. 42,858

"Express Mail" mailing label number: EV370238815US

Date of Deposit: February 9, 2004

This paper or fee is being deposited on the date indicated above with the United States Postal Service pursuant to 37 CFR 1.10, and is addressed to the Commissioner for Patents, Mail Stop Patent Application, P.O. Box 1450, Alexandria, VA 22313-1450.

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)	Complete if Known	
	Application Number	Unknown
	Filing Date	Even Date Herewith
	First Named Inventor	Gardner, Donald
	Group Art Unit	Unknown
	Examiner Name	Unknown
Sheet 1 of 1		Attorney Docket No: 884.140US3

US PATENT DOCUMENTS						
Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
	US-3,881,244	05/06/1975	Kendall, Don L.	29	602	08/03/1973
	US-5,095,357	03/10/1992	Andoh, N., et al.	357	51	08/14/1990
	US-5,446,311	08/29/1995	Ewen, John, et al.	257	508	09/16/1994
	US-5,635,892	06/03/1997	Ashby, K. B., et al.	336	200	12/06/1994
	US-5,753,391	05/19/1998	Stone, M. D., et al.	430	22	09/27/1995
	US-5,801,100	09/01/1998	Lee, C., et al.	438	678	03/07/1997
	US-5,877,533	03/02/1999	Arai, M., et al.	257	350	03/06/1997
	US-5,952,704	09/14/1999	Yu, et al.	257	528	04/17/1997
	US-6,037,649	03/14/2000	Liou, Ping	257	531	04/01/1999
	US-6,040,226	03/21/2000	Wojnarowski, R.J., et al.	438	381	10/23/1998
	US-6,114,937	09/05/2000	Burghartz, J. N., et al.	336	200	10/14/1997
	US-6,201,287	03/13/2001	Forbes, L.	257	528	10/26/1998
	US-6,240,621	01/05/2001	Nellissen, A. J., et al.	29	602.1	08/04/1998
	US-6,281,560	08/28/2001	Allen, Mark, et al.	257	414	06/22/1998
	US-6,291,305	09/18/2001	Huang, Chi-Jung, et al.	438	381	06/11/1999
	US-6,344,125	02/05/2002	Locke, P. S., et al.	205	118	04/06/2000

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ²
	EP-0725407	08/07/1996	Nmh Hsieh, Chang-Ming, et al.	H01F	17/00	
	JP-07272932	10/20/1995	Terayma, Yoshimi	H01F	17/00	

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		SHIRAKAWA, K., et al., "Thin film cloth-structured inductor for magnetic integrated circuit", <u>IEEE Transactions on Magnetics</u> , Vol. 26, No. 5, XP000150520, (Sept. 1990), 2262-2264	
		YAMAGUCHI, M., et al., "Characteristics of magnetic thin-film inductors at large magnetic field", <u>Proceedings of the Int'l Magnetics Conf.</u> , XP000581941, (April 18, 1995), BS-1	

EXAMINER

DATE CONSIDERED